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AMENDMENTS TO THE SPECIFICATION

In the Specification:

Please replace paragraph [0012] on page 3, with the following rewritten paragraph:

[0012] Some embodiments of the invention may be implemented, for example, using a machine-readable medium or article which may store an instruction ora or a set of instructions that, if executed by a machine (for example, by station 110, and/or by other suitable machines), cause the machine to perform a method and/or operations in accordance with embodiments of the invention. Such machine may include, for example, any suitable processing platform, computing platform, computing device, processing device, computing system, processing system, computer, processor, or the like, and may be implemented using any suitable combination of hardware and/or software. The machine-readable medium orarticle or article may include, for example, any suitable type of memory unit, memory device, memory article, memory medium, storage device, storage article, storage medium and/or storage unit, for example, memory, removable or non-removable media, crasable or non-erasable media, writcable or re-writeable media, digital or analog media, hard disk, floppy disk, Compact Disk Read Only Memory (CD-ROM), Compact Disk Recordable (CD-R), Compact Disk Rewriteable (CD-RW), optical disk, magnetic media, various types of Digital Versatile Disks (DVDs), a tape, a cassette, or the like. The instructions may include any suitable type of code, for example, source code, compiled code, interpreted code, executable code, static code, dynamic code, or the like, and may be implemented using any suitable high-level, low-level, object-oriented, visual, compiled and/or interpreted programming language, e.g., C, C++, Java, high level design programming language, assembly language, machine code, or the like.

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Please replace paragraph [0014] on page 4, with the following rewritten paragraph:

[0014] Although the scope of the present invention is not limited in this respect, links, such as for example, an uplink and a downlink, may be used to transfer communications between base station 110 and mobile station 120. An uplink 130 may transfer communications from mobile station 120 to base station 110, and a downlink 140 may transfer communications from base station 110 to mobile station 120. Additionally, uplink 130 and downlink 140 may include one or more channels, which may be used for voice and data transportation. Furthermore, channels may carry signals, which may include symbols that include chips. Some of the signals may be spread over two or more multipaths and the symbols may be spread because a delay between the multipaths components. In addition, multipath components may be referred referred as diversity signals. For example, in those signals a symbol may be spread because of delays between other diversity signals. Signals providing diversity may include symbols that arise because of the multipath signals between a transmit antenna and a receiver, symbols that arise because of multiple transmit antennas at a single transmitter, and/or symbols that arise because of multiple transmitters. For example, multiple transmitters may provide diversity signals when performing a soft-handoff in a downlink CDMA system where multiple base stations may transmit the same symbol to a desired rcceiver.

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Please replace paragraph [0019] on page 6, with the following rewritten paragraph:

[0019] Turning to FIG. 3, a block diagram of a communication device 300, according to one exemplary embodiment of the present invention is shown. Although the scope of the present invention is not limited in this respect, communication device 300 may be, for example, a cellular radio telephone device, a wireless local area (WLAN), a base station of a cellular system, or the like. Communication device 300 may include: an antenna to receive and/or send signals; a receiver (RX) 320, for example, a CDMA receiver, a WCDMA receiver, or the like; a detector 330; a processor 340; and a memory 350. Although the scope of the present invention is not limited in this respect, decoder 330 may include a desired number of processing windows units, for example processing windows units 333, 334, 335 as shown, and a combiner 337 339.

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Please replace paragraph [0030] on page 10, with the following rewritten paragraph:

[0030] Although the scope of the present invention is not limited in this respect, in some other embodiments of the invention, delays that may be generated by delay unit 435 and delay unit 437 may be provided by a memory device, e.g., memory 450, if desired. For example, the output of processing window unit 432 may be stored for the time needed to process the desired symbol in different multipath component groups. In this example, combiner 439 may receive the processed output of processing window unit 432 of the multipath component groups from a memory with the respective delays applied by delay units 435 and 439 437, if desired.

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AMENDMENTS TO THE DRAWINGS

The attached sheet of drawings includes changes to Fig. 1. This sheet, which includes Fig. 1, replaces the original sheet including Fig. 1. In Figure 1, the legend PRIOR ART has been added.

Attachment: Replacement Sheet